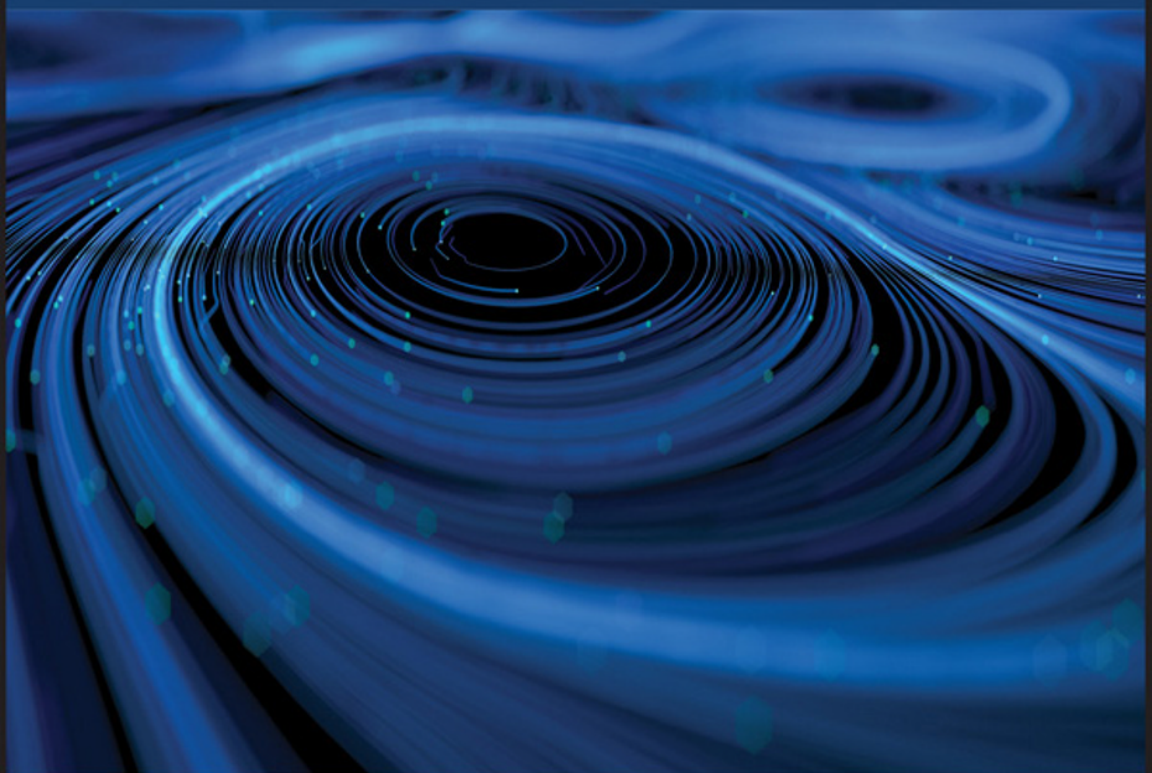


TSAI-FU WU | YU-KAI CHEN

# ORIGIN OF POWER CONVERTERS

DECODING, SYNTHESIZING, AND MODELING



WILEY



## **Origin of Power Converters**



# **Origin of Power Converters**

Decoding, Synthesizing, and Modeling

*Tsai-Fu Wu*

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*To Our Families*

*Ching-Ying, Charles and Jerry*

*Yun-Wen and Allen*





## Contents

<b>Preface</b>	<i>xv</i>
<b>Acknowledgments</b>	<i>xvii</i>
<b>About the Authors</b>	<i>xviii</i>

### Part I Decoding and Synthesizing 1

<b>1</b>	<b>Introduction</b>	<b>3</b>
1.1	Power Processing Systems	4
1.2	Non-PWM Converters Versus PWM Converters	7
1.2.1	Non-PWM Converters	7
1.2.2	PWM Power Converters	9
1.3	Well-Known PWM Converters	10
1.4	Approaches to Converter Development	17
1.5	Evolution	25
1.6	About the Text	26
1.6.1	Part I: Decoding and Synthesizing	26
1.6.2	Part II: Modeling and Applications	28
	Further Reading	28
<b>2</b>	<b>Discovery of Original Converter</b>	<b>31</b>
2.1	Creation of Original Converter	31
2.1.1	Source–Load Approach	32
2.1.2	Proton–Neutron–Meson Analogy	32
2.1.3	Resonance Approach	33
2.2	Fundamental PWM Converters	34
2.2.1	Voltage Transfer Ratios	35
2.2.2	CCM Operation	36
2.2.3	DCM Operation	38

2.2.4 Inverse Operation 39

2.3 Duality 40

Further Reading 41

### **3 Fundamentals 43**

3.1 DC Voltage and Current Offsetting 43

3.1.1 DC Voltage Offsetting 44

3.1.2 DC Current Offsetting 47

3.2 Capacitor and Inductor Splitting 49

3.3 DC-Voltage Blocking and Pulsating-Voltage Filtering 51

3.4 Magnetic Coupling 55

3.5 DC Transformer 58

3.6 Switch Grafting 62

3.7 Diode Grafting 67

3.8 Layer Scheme 72

Further Reading 74

### **4 Decoding Process 77**

4.1 Transfer Ratios (Codes) 77

4.2 Transfer Code Configurations 82

4.2.1 Cascade Configuration 82

4.2.2 Feedback Configuration 82

4.2.3 Feedforward Configuration 83

4.2.4 Parallel Configuration 85

4.3 Decoding Approaches 86

4.3.1 Factorization 86

4.3.2 Long Division 88

4.3.3 Cross Multiplication 89

4.4 Decoding of Transfer Codes with Multivariables 91

4.5 Decoding with Component-Interconnected Expression 93

Further Reading 94

### **5 Synthesizing Process with Graft Scheme 95**

5.1 Cell Approaches 95

5.1.1 P-Cell and N-Cell 96

5.1.2 Tee Canonical Cell and Pi Canonical Cell 97

5.1.3 Switched-Capacitor Cell and Switched-Inductor Cell 98

5.1.4 Inductor-Capacitor Component Cells 100

5.2 Converter Grafting Scheme 101

5.2.1 Synchronous Switch Operation 101

5.2.2 Grafting Active Switches 103

5.2.3	Grafting Passive Switches	108
5.3	Illustration of Grafting Converters	110
5.3.1	Grafting the Well-Known PWM Converters	110
5.3.1.1	Graft Boost on Buck	111
5.3.1.2	Graft Buck on Boost	112
5.3.1.3	Graft Buck on Buck–Boost	114
5.3.1.4	Graft Boost on Boost–Buck	116
5.3.1.5	Buck in Parallel with Buck–Boost	119
5.3.1.6	Grafting Buck on Buck to Achieve High Step-Down Voltage Conversion	119
5.3.1.7	Grafting Boost on Boost to Achieve High Step-up Voltage Conversion	120
5.3.1.8	Grafting Boost (CCM) on Buck (DCM)	121
5.3.1.9	Cascode Complementary Zeta with Buck	123
5.3.2	Grafting Various Types of Converters	124
5.3.2.1	Grafting Half-Bridge Resonant Inverter on Dither Boost Converter	124
5.3.2.2	Grafting Half-Bridge Resonant Inverter on Bidirectional Flyback Converter	124
5.3.2.3	Grafting Class-E Converter on Boost Converter	125
5.3.3	Integrating Converters with Active and Passive Grafted Switches	127
5.3.3.1	Grafting Buck on Boost with Grafted Diode	128
5.3.3.2	Grafting Half-Bridge Inverter on Interleaved Boost Converters in DCM	128
5.3.3.3	Grafting $N$ -Converters with TGS	130
5.3.3.4	Grafting $N$ -Converters with IIGS	130
	Further Reading	132

## **6 Synthesizing Process with Layer Scheme 133**

6.1	Converter Layering Scheme	133
6.2	Illustration of Layering Converters	135
6.2.1	Buck Family	135
6.2.2	Boost Family	138
6.2.3	Other Converter Examples	142
6.3	Discussion	146
6.3.1	Deduction from Ćuk to Buck–Boost	146
6.3.2	Deduction from Sepic to Buck–Boost	148
6.3.3	Deduction from Zeta to Buck–Boost	149
6.3.4	Deduction from Sepic to Zeta	150
	Further Reading	151

<b>7</b>	<b>Converter Derivation with the Fundamentals</b>	<b>153</b>
7.1	Derivation of Buck Converter	153
7.1.1	Synthesizing with Buck–Boost Converter	154
7.1.2	Synthesizing with Ćuk Converter	154
7.2	Derivation of z-Source Converters	154
7.2.1	Voltage-Fed z-Source Converters	155
7.2.1.1	Synthesizing with Sepic Converter	157
7.2.1.2	Synthesizing with Zeta Converter	160
7.2.2	Current-Fed z-Source Converters	161
7.2.2.1	Synthesizing with SEPIC Converter	162
7.2.2.2	Synthesizing with Zeta Converter	162
7.2.3	Quasi-z-Source Converter	162
7.2.3.1	Synthesizing with Sepic Converter	164
7.2.3.2	Synthesizing with Zeta Converter	165
7.3	Derivation of Converters with Switched Inductor or Switched Capacitor	166
7.3.1	Switched-Inductor Converters	167
7.3.1.1	High Step-Down Converter with Transfer Code $D/(2 - D)$	167
7.3.1.2	High Step-Down Converter with Transfer Code $D/(2(1 - D))$	173
7.3.2	Switched-Capacitor Converters	178
7.3.2.1	High Step-Up Converter with Transfer Code $(1 + D)/(1 - D)$	178
7.3.2.2	High Step-Up Converter with Transfer Code $2D/(1 - D)$	181
7.3.2.3	High Step-Up Converter with Transfer Code $D/(1 - 2D)$	184
7.4	Syntheses of Desired Transfer Codes	185
7.4.1	Synthesis of Transfer Code: $D^2/(D^2 - 3D + 2)$	186
7.4.1.1	Synthesizing with Buck–Boost Converter	187
7.4.1.2	Synthesizing with Zeta Converter	188
7.4.1.3	Synthesizing with Ćuk Converter	189
7.4.2	Synthesizing Converters with the Fundamentals	191
7.4.2.1	DC Voltage and DC Current Offsetting	191
7.4.2.2	Inductor and Capacitor Splitting	192
7.4.2.3	DC Voltage Blocking and Filtering	192
7.4.2.4	Magnetic Coupling	193
7.4.2.5	DC Transformer	194
7.4.2.6	Switch and Diode Grafting	195
7.4.2.7	Layer Technique	195
	Further Reading	198
<b>8</b>	<b>Synthesis of Multistage and Multilevel Converters</b>	<b>199</b>
8.1	Review of the Original Converter and Its Variations of Transfer Code	199

8.2	Syntheses of Single-Phase Converters	201
8.3	Syntheses of Three-Phase Converters	203
8.4	Syntheses of Multilevel Converters	207
8.5	$L$ - $C$ Networks	210
	Further Reading	212
<b>9</b>	<b>Synthesis of Soft-Switching PWM Converters</b>	<b>215</b>
9.1	Soft-Switching Cells	215
9.1.1	Passive Lossless Soft-Switching Cells	216
9.1.1.1	Near-Zero-Current Switching Mechanism	216
9.1.1.2	Near-Zero-Voltage Switching Mechanism	218
9.1.2	Active Lossless Soft-Switching Cells	220
9.1.2.1	Zero-Voltage Switching Mechanism	222
9.1.2.2	Zero-Current Switching Mechanism	226
9.2	Synthesis of Soft-Switching PWM Converters with Graft Scheme	230
9.2.1	Generation of Passive Soft-Switching PWM Converters	230
9.2.2	Generation of Active Soft-Switching PWM Converters	234
9.3	Synthesis of Soft-Switching PWM Converters with Layer Scheme	240
9.3.1	Generation of Passive Soft-Switching PWM Converters	240
9.3.2	Generation of Active Soft-Switching PWM Converters	245
9.4	Discussion	247
	Further Reading	251
<b>10</b>	<b>Determination of Switch-Voltage Stresses</b>	<b>255</b>
10.1	Switch-Voltage Stress of the Original Converter	255
10.2	Switch-Voltage Stresses of the Fundamental Converters	257
10.2.1	The Six Well-Known PWM Converters	257
10.2.1.1	Boost Converter	257
10.2.1.2	Buck-Boost Converter	258
10.2.1.3	Ćuk, Sepic, and Zeta Converters	259
10.2.2	$z$ -Source Converters	260
10.2.2.1	Voltage-Fed $z$ -Source Converter	260
10.2.2.2	Current-Fed $z$ -Source Converter	261
10.2.2.3	Quasi- $z$ -Source Converter	262
10.3	Switch-Voltage Stresses of Non-Fundamental Converters	263
10.3.1	High Step-Down Switched-Inductor Converter	263
10.3.2	High Step-Down/Step-Up Switched-Inductor Converter	264
10.3.3	Compound Step-Down/Step-Up Switched-Capacitor Converter	265

- 10.3.4 High Step-Down Converter with Transfer Ratio of  $D^2$  267
- 10.3.5 High Step-Up Converter with Transfer Ratio of  $1/(1 - D)^2$  268
- Further Reading 270

## **11 Discussion and Conclusion 271**

- 11.1 Will Identical Transfer Code Yield the Same Converter Topology? 271
- 11.2 Topological Duality Versus Circuital Duality 274
- 11.3 Graft and Layer Schemes for Synthesizing New Fundamental Converters 277
  - 11.3.1 Synthesis of Buck–Boost Converter 278
  - 11.3.2 Synthesis of Boost–Buck (Ćuk) Converter 279
  - 11.3.3 Synthesis of Buck–Boost–Buck (Zeta) Converter 280
  - 11.3.4 Synthesis of Boost–Buck–Boost (Sepic) Converter 282
  - 11.3.5 Synthesis of Buck-Family Converters with Layer Scheme 284
  - 11.3.6 Synthesis of Boost-Family Converters with Layer Scheme 286
- 11.4 Analogy of Power Converters to DNA 289
  - 11.4.1 Replication 291
  - 11.4.2 Mutation 291
- 11.5 Conclusions 295
- Further Reading 296

## **Part II Modeling and Application 299**

### **12 Modeling of PWM DC/DC Converters 301**

- 12.1 Generic Modeling of the Original Converter 302
- 12.2 Series-Shunt and Shunt-Series Pairs 303
- 12.3 Two-Port Network 308
- 12.4 Small-Signal Modeling of the Converters Based on Layer Scheme 315
- 12.5 Quasi-Resonant Converters 323
- Further Reading 326

### **13 Modeling of PWM DC/DC Converters Using the Graft Scheme 329**

- 13.1 Cascade Family 330
- 13.2 Small-Signal Models of Buck-Boost and Ćuk Converters Operated in CCM 332
  - 13.2.1 Buck-Boost Converter 336
  - 13.2.2 Boost-Buck Converter 338
- 13.3 Small-Signal Models of Zeta and Sepic Operated in CCM 340

13.3.1	Zeta Converter	344
13.3.2	Sepic Converter	346
	Further Reading	349
<b>14</b>	<b>Modeling of Isolated Single-Stage Converters with High Power Factor and Fast Regulation</b>	<b>351</b>
14.1	Generation of Single-Stage Converters with High Power Factor and Fast Regulation	352
14.2	Small-Signal Models of General Converter Forms Operated in CCM/DCM	355
14.3	An Illustration Example	361
	Further Reading	365
<b>15</b>	<b>Analysis and Design of an Isolated Single-Stage Converter Achieving Power Factor Correction and Fast Regulation</b>	<b>367</b>
15.1	Derivation of the Single-Stage Converter	368
15.1.1	Selection of Individual Semi-Stages	369
15.1.2	Derivation of the Discussed Isolated Single-Stage Converter	369
15.2	Analysis of the Isolated Single-Stage Converter Operated in DCM + DCM	369
15.2.1	Buck-Boost Power Factor Corrector	370
15.2.2	Flyback Regulator	372
15.3	Design of a Peak Current Mode Controller for the ISSC	373
15.4	Practical Consideration and Design Procedure	377
15.4.1	Component Stress	377
15.4.2	Snubber Circuit	378
15.4.3	Design Procedure	379
15.5	Hardware Measurements	380
15.6	Design of an $H^\infty$ Robust Controller for the ISSC	382
15.6.1	$H^\infty$ Control	382
15.6.2	An Illustration Example of Robust Control and Hardware Measurements	386
	Further Reading	392
	<b>Index</b>	<b>395</b>





## Preface

This book is divided into two parts. Part I presents evolution and development of power converters from the original converter. Hundreds of power converter topologies have been developed over past one century by many researchers. However, there is no single systematic approach to developing the converters. Inspired by Charles Darwin who published the book entitled *The Origin of Species* and based on the principle of resonance, we identify the original converter, on which we develop the mechanisms of evolution, decoding, and synthesizing processes, to derive PWM power converters systematically. With the decoding process, the input-to-output transfer codes (ratios) are decoded into code configurations in terms of the transfer codes derived from the original converter. With the synthesizing process, we have developed the graft and layer schemes, which are used in growing plants, along with circuit fundamentals to synthesize the code configurations into converters. With these two processes, illustrations of the existing and newly developed hard-switching and soft-switching PWM converters, including the well-known *z*-source converters, Vienna converters, modular multilevel converters, switched-inductor/switched-capacitor converters, *etc.*, are presented in detail. Additionally, determination of converters' switch-voltage stresses based on their transfer codes is addressed. Moreover, based on the principle of resonance, the well-known six PWM converters are reconfigured, and analogy of PWM converters to DNA is presented, from which mutation and replication of PWM converters are discussed.

Part II presents modeling and applications of power converters based on the original converter and the developed graft and layer schemes. The six PWM converters can be modeled into families represented in two-port networks. Therefore, relationships among the converters can be identified and the modeling processes can be simplified. In addition, single-stage converters to fulfill multiple functions are derived and modeled, on which two application examples are presented and verified with experimental results.

Since Charles Darwin in 1859 initiated an evolution principle, through around one hundred years and many researchers' study, Gregor J. Mendel developed the laws of inheritance in 1866, Boveri-Sutton developed chromosome theory in 1902, and James D. Watson discovered the double-helix structure of DNA in 1953, affecting significantly the followed genetic engineering innovations. Like Charles Darwin, we initiate an evolution of power converters, and we do expect other researchers can follow this stepstone to go further. This does not conclude the work, but just gets started.

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## Acknowledgments

This book collects most of our work in converter development and modeling over past 25 years. We are grateful to our former PhD and Master students who have contributed to this book, especially Dr Te-Hung Yu and Dr Frank Liang. We are also thankful to the Ministry of Science and Technology, Taiwan, for constantly funding our research work. Our special appreciation goes to Cecilia Wang and Ya-Fen Cheng who edit the book prudently to meet the requirements from Wiley Publisher.

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## **Part I**

### **Decoding and Synthesizing**



# 1

## Introduction

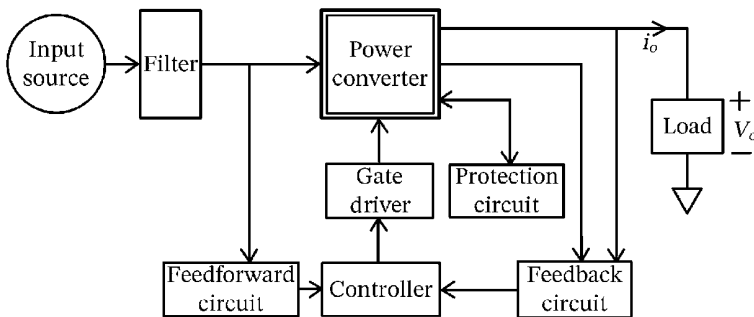
Electrical energy has been widely applied, and its growth rate has been increasing dramatically over the past two decades. In particular, renewable energy coming to play has driven electricity utilization and processing needs to reach another growth peak. Additionally, machine electrification and factory automation have also increased the demand of electricity. With increasing use of sophisticated equipment and instruments, high power quality becomes just essential. To supply sufficient, of high quality, and stable electrical power in desired voltage or current forms, power processing systems are indispensable. Meanwhile, they also play an important role in supporting continuous growth of human beings' civilization, environmental conservation, and energy harvesting. In designing a power processing system, the first step needs to select a power converter topology since the converter topology mainly governs the fundamental properties, such as step-up, step-down, bipolar operation, component stresses, etc. Converters come out with very diversified configurations. How to derive or develop them systematically without trial and error is an interesting topic. Thus, many researchers have devoted in developing power converter topologies for various types of applications.

In this chapter, configuration of a power processing system is first addressed. Fundamental two types of power converter classifications, general pulse-width modulated (PWM) converters and non-PWM ones, are presented. Then, the well-known PWM converters are introduced for later comparison and illustration. In literature, there are many approaches to developing power converters, and their fundamental principles will be described briefly. In addition, an evolution concept is presented for illustrating later converter derivation. A section introducing the overall organization of this book will be included in the end of this chapter.

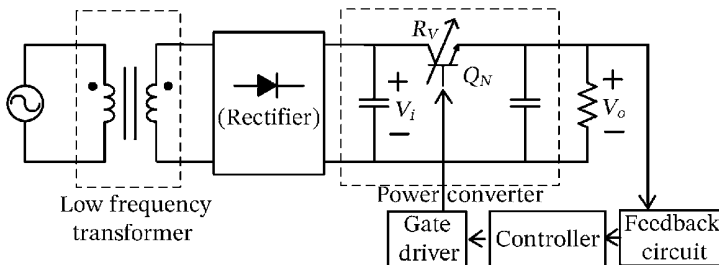
## 1.1 Power Processing Systems

Configuration of a power processing system can be illustrated in Figure 1.1, which mainly includes input filter, power converter, feedback/feedforward circuits, controller, gate driver, and protection circuit. The input source can be obtained from utility outlet/grid or renewable energy generators, such as photovoltaic panel, wind turbine, geothermal heat pump, etc. Its voltages and currents can be any form, and their amplitudes might vary with time or fluctuate frequently. At the output side, the load may require various voltage and current forms, too. Thus, a proper power converter topology along with a promising controller is usually required to realize a power processing system.

Conventionally, the conceptual block diagram of a power processing system shown in Figure 1.1 can be realized by the circuit shown in Figure 1.2, which, as an example, is a linear regulator. In the circuit, semiconductor switch  $Q_N$  is operated in linear region to act as a variable resistor, which can absorb the voltage difference between input voltage  $V_i$  and output voltage  $V_o$  and in turn regulate  $V_o$  under load variation. The primary merits of a linear regulator include low output voltage ripple and low noise interference. However, it has many drawbacks, such



**Figure 1.1** Configuration of a power processing system.



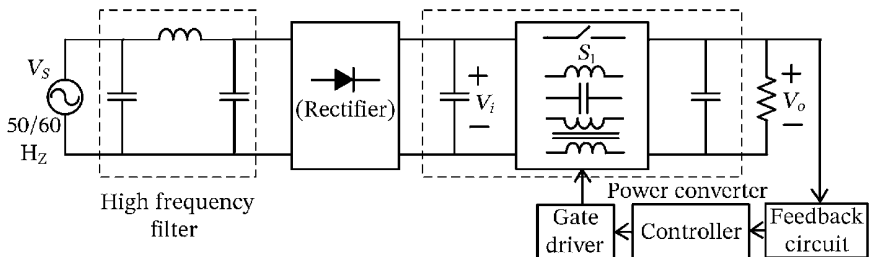
**Figure 1.2** Block diagram of a linear regulator.



as the transformer with low operating frequency results in bulky size and heavy weight, semiconductor  $Q_N$  operating in linear region results in high power loss and low efficiency, and the low efficiency requires a large, heavy heat sink and even needs forced ventilation. These drawbacks have limited its wide applications to compact electronic products, renewable power generators, and energy harvesters, where efficiency and size are the essential concerns.

To improve efficiency and release the aforementioned limitations, switching power regulators were developed. A typical configuration of the switching regulators is shown in Figure 1.3, where in the power converter, switch  $M_1$  is operated in saturation region (if using BJT as a switch) or in ohmic region (if using MOSFET as a switch), reducing conduction loss dramatically. At the input side, the corner frequency of the filter is close to switching frequency, and its size and weight can be also reduced significantly. If isolation is required, high frequency transformer will be introduced to the converter, and its size and weight are relatively small as compared with a low-frequency one (operating at 50/60 Hz). In general, a switching regulator has the merits of high power density, small volume, low weight, improved efficiency, and cost and component reduction. There still exist several limitations, such as resulting in high switching noise, increasing analysis and design complexity, and requiring sophisticated control. Although switching regulators have the limitations, thanks to recent advances in high efficiency and high frequency component development, nanoscale integrated circuit (IC) fabrication technique, and analysis tool, they have been widely applied to electronic products, energy harvesting, and power quality improvement. For further discussion, we will focus on switching regulators only.

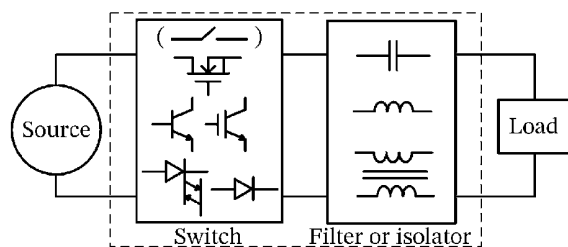
For a switching regulator or a more general term, switching power converter, the input source can be either AC or DC form, and the output load can be also supplied by either AC or DC form. Thus, there are four types of combinational forms in classifying power converter topologies, which are AC to DC, AC to AC, DC to AC, and DC to DC. In Figure 1.3, the *rectifier* converts AC to DC, and the *power converter* converts DC to DC. Typically, a power processing system may



**Figure 1.3** Block diagram of a switching regulator.

need multiple power converters to collaborate each other, but they might be integrated into a single power stage for certain applications, such as a notebook adapter consisting of a rectifier (AC to DC), a power factor corrector (DC to DC), and an isolated regulator (DC to DC), which can be integrated into a bridgeless isolated regulator. A power converter requires at least a control gear or switch to control power flow between source and load, and it might need some buffers or filters to smooth and hold up voltage and current, which can be illustrated in Figure 1.4. The switch can be realized with BJT, MOSFET, IGBT, GTO, etc. along with freewheeling diodes. It is worth noting that recent advances in wide-band-gap switching device development, such as SiC and GaN, have merited to switching power converters because their switching losses have been reduced significantly. The buffer or filter is realized with capacitor alone or capacitor-inductor pair. If it requires galvanic isolation, a transformer is introduced into the converter. Additionally, the transformer provides another degree of freedom in tuning input-to-output voltage ratio and can implement multiple outputs readily. To fulfill multiple functions or increase power capacity, converters can be connected in series or parallel, which will complicate analysis, design, and control.

As shown in Figure 1.4, connecting switch(es) and capacitors/inductors to form a power converter sounds simple. However, how to configure a power converter to achieve step-up, step-down, and step-up/step-down DC output, AC output, PWM control, variable frequency control, etc. is not an easy task. Even with the same step-up/step-down transfer ratio, there exist different converter topologies, and they might have different dynamic performances and different component stresses. Among the four types of power converter topologies is the DC to DC, simplified to DC/DC, converter type relatively popular. In the following, we will first present how to figure out the derivation of DC/DC converter topologies, on which the rest of converter types will be discussed. Exploring systematic approaches to developing power converter topologies is the unique feature of this book.



**Figure 1.4** Possible components in a power converter.

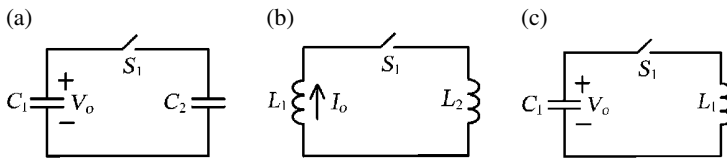
## 1.2 Non-PWM Converters Versus PWM Converters

In power converters, when switch turns on with infinite current through or infinite voltage across components, this is because there is no current-limiting or voltage-blocking components in the conduction path, resulting in severe electro-magnetic interference (EMI) problems. This type of power converter cannot be controlled with PWM and is called a non-PWM converter. On the contrary, there exist current-limiting and voltage-blocking components in the conduction path of a power converter, and it can be controlled with PWM, which is called a PWM converter. This claim will be presented and illustrated with some power converter examples, as follows.

### 1.2.1 Non-PWM Converters

The major concern of a power converter is its input–output conversion efficiency. In practice, there is no resistor allowed in a converter configuration. A qualified converter includes only ideal switch(es) and capacitor(s)/inductor(s). However, even with these components only, there might still exist loss during power transfer, such as the converters shown in Figure 1.5a and b. Figure 1.5a shows power transfer between two capacitors, and it is controlled by switch  $S_1$ . Assuming capacitor  $C_1$  is associated with an initial voltage of  $V_o$  and  $C_2$  is with zero voltage, and capacitance  $C_1 = C_2$ , it can be shown that there is an electrical energy loss,  $(1/4)C_1V_o^2$ , which is half the initially stored energy in  $C_1$ . Moreover, when switch  $S_1$  is turned on, an inrush current flows from  $C_1$  to  $C_2$  and through  $S_1$  in almost no time, which may damage the components and cause EMI problems. For this type of circuit configuration, the only current limiter is the equivalent inductance and resistance of the components and the circuit path. It can be said that there is no control on the capacitor currents and voltages, and the voltages of both capacitors  $C_1$  and  $C_2$  will be always balanced at  $(1/2)V_o$ . This type of power converter configuration is classified as a non-PWM converter.

Similarly, the conceptual inductor–inductor–switch configuration shown in Figure 1.5b has the same limitations. If, initially, inductor  $L_1$  carries a current of  $I_o$

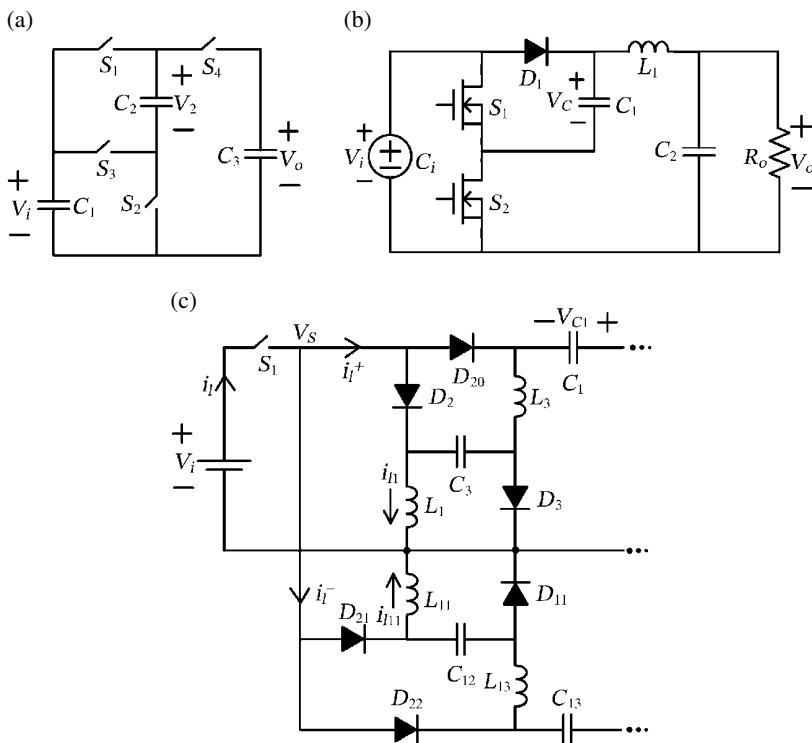


**Figure 1.5** (a) Capacitor–capacitor–switch, (b) inductor–inductor–switch, and (c) capacitor–inductor–switch networks.

but there is no current in  $L_2$ , after turning on switch  $S_1$ , there will be an extremely high impulse voltage across the inductors and the switch, causing EMI problems and damage to the components. Again, there is half electrical energy loss  $(1/4)L_1 I_o^2$  if  $L_1 = L_2$ , and there is no control on the inductor voltages and currents at all. It is also a non-PWM converter.

In summary, non-PWM converters come out high inrush current or high impulse voltage, resulting in high EMI, as well as high component stress, and they could yield low conversion efficiency even with ideal components. In particular, under large initial voltage difference, the maximum electrical energy loss can be as high as 50%.

Other examples adopting the configuration shown in Figure 1.5a are shown in Figure 1.6. Figure 1.6a shows a two-lift converter. When switches  $S_1$  and  $S_2$  are turned on, capacitor  $C_1$  will charge  $C_2$  directly. On the other hand, when switch  $S_3$  and  $S_4$  are turned on, capacitors  $C_1$  and  $C_2$  are connected in series to charge capacitor  $C_3$  and lift the output voltage  $V_o$  to be twice the input voltage  $V_i$ . It can be seen



**Figure 1.6** Non-PWM converters: (a) two lift, (b) KY, and (c) re-lift circuit.

that during capacitor charging, there is no current limiter, resulting in high inrush current. Figure 1.6b shows the KY converter. When switch  $S_2$  is turned on, input voltage  $V_i$  will charge capacitor  $C_1$  through diode  $D_1$  but again without current limiter. When switch  $S_2$  is turned off and  $S_1$  is turned on, input voltage  $V_i$  together with capacitor voltage  $V_C$  will magnetize inductor  $L_1$  through the output path. This path of power flow is with the current limiter of inductor  $L_1$ . Figure 1.6c shows a re-lift converter. When switch  $S_1$  is turned on, there are two capacitor charging paths without current limiter,  $V_i-S_1-D_2-C_3-D_3-V_i$  and  $V_i-S_1-D_{21}-C_{12}-D_{11}-V_i$ . When switch  $S_1$  is turned off, the energy stored in capacitors  $C_3$  and  $C_{12}$  will be released to the output through the inductors and capacitors, which are the current limiters.

With a non-PWM converter, the processed power level is usually pretty low because of high inrush current or high pulse voltage. It can be used for supplying integrated circuits, which require low power consumption, of which the low current rating switches have high conduction resistance and act as current limiters. For high power processing, we need PWM power converters.

### 1.2.2 PWM Power Converters

Power transfer between a capacitor and an inductor can be modulated by a switch, as shown in Figure 1.5c, and their total electrical energy is always conserved to their initially stored energy. In the network, capacitor  $C_1$  limits the slew rate of voltage variation, inductor  $L_1$  limits that of current variation, and switch  $S_1$  controls the time interval of power transfer, i.e., pulse-width modulation. Thus, component stresses can be properly controlled, and high conversion efficiency can be insured. Additionally, EMI level can be also reduced significantly. Power converter configurations based on this type of network are called PWM power converters. Note that it requires an additional freewheeling path when switch  $S_1$  is turned off, which will be discussed in later section. For simplicity while without confusion in power electronics area, the short-form PWM converters or converters will be used to represent the PWM power converters. They have been widely applied to various types of power conversion for their controllable power transfer, theoretically no loss, and finite component stresses.

The minimum-order network of a PWM converter is a second-order  $LC$  network, and it must at least include a switch to control power flow. The order of network can be increased to third, fourth, and even higher. For a valid PWM converter, the network must be always in resonant manner at either switch turn-on or turn-off.

Over the past century, PWM converters have been well developed and have diversified configurations, such as buck, boost, buck-boost, Ćuk, sepic, Zeta, fly-back, forward, push-pull, half-bridge, full-bridge, Z-source, neutral-point clamped (NPC), modular multilevel, quasi-resonant, and  $LLC$  resonant converters. They

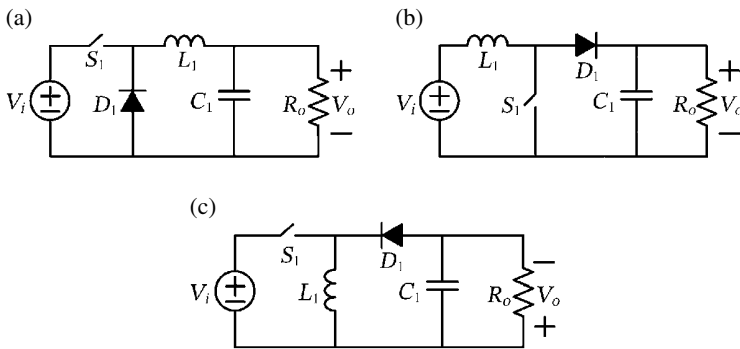
can be classified into non-isolated and isolated configurations. Typically, the isolated versions can be derived from the non-isolated ones by inserting a DC transformer or an AC transformer to a proper location of the converter. Thus, we will first introduce non-isolated converters, which can lay out a firm foundation for later discussions on isolated converters.

### 1.3 Well-Known PWM Converters

Almost all people entering power electronics field know about buck, boost, and buck-boost converters, as shown in Figure 1.7. To my best knowledge, it is unknown that who invented the buck converter and when it was invented. Since electricity started to be used frequently between the late nineteenth century and the early twentieth century, the invention of the buck converter was designated as year 1900. The boost converter was invented during World War II, which was used to boost voltage for transmitting radio signals across Atlantic Ocean. The buck-boost converter was invented around 1950.

Analyzing their operational principles will realize that the buck, boost, and buck-boost converters can achieve step-down, step-up, and step-down/step-up input-to-output voltage conversions, respectively. They all have a second-order  $LC$  network and a pair of active-passive switches but have different circuit configurations.

If we explore further, there are another three famous converters, and each of which has a fourth-order  $LC$  network and a pair of active-passive switches, as shown in Figure 1.8, in which they have different circuit configurations, but they all can fulfill the same step-down/step-up voltage conversion. Ćuk converter was



**Figure 1.7** Power converters with a second-order  $LC$  network and a pair of active-passive switches: (a) buck converter, (b) boost converter, and (c) buck-boost converter.